

# 6.3 GHz Compact USB Real-Time Spectrum Analyzer

## SAN-60 M2

### Product Brochure V0.6

2023-10-18

Distribution in the UK & Ireland



[www.lambdaphoto.co.uk](http://www.lambdaphoto.co.uk)

- Outstanding device performance and price advantage
- 9 kHz~6.3 GHz real-time spectrum analyzer
- Integrated 100 kHz-6.3 GHz analog signal generator (opt.)
- 25 MHz analysis bandwidth, 100 GHz/s sweep speed, FPGA signal processing
- 1GHz phase noise: -110 dBc/Hz@10 kHz
- Equipped with preamplifier, 1GHz DANL: -162 dBm/Hz
- Core module supported, light as 168 g, 142x54x16 mm, power consumption 7~10 W
- Highly compatible API interfaces (HTRA API)and SASstudio4 GUI
- Compatible with ARM and x86 processors, Linux and Windows operating systems
- Operating temperatures range from - 20 °C/- 40 °C to 65 °C (opt.)
- Built-in OCXO (option), temperature drift≤0.15 ppm
- USB3.0/2.0 Type-C interface

## SAN-60 M2 Technical Specifications \* (typical value)

Indicator test basis    Hardware Version: R5    API: 0.55.12    FPGA: 0.55.2    MCU: 0.55.9    SAS4: 1.55.57

Frequency				
Frequency Range	9 kHz~6.3 GHz			
Initial Frequency Accuracy	<1 ppm, supporting program manual correction			
Reference Clock	Internal or external, program-controlled switching, internal TCXO aging<1 ppm/year, temperature drift≤1 ppm; internal OCXO (option), temperature drift≤0.15 ppm			
Disciplined GNSS	Support external GNSS (option) disciplines and recalculates the built-in reference clock			
Spectrum Purity				
SSB Phase Noise	dBc/Hz			
Carrier Frequency	500 MHz	1 GHz	3 GHz	6 GHz
1 kHz	-110.3	-105.0	-97.5	-91.2
10 kHz	-118.4	-110.4	-101.2	-99.3
100 kHz	-118.1	-110.5	-100.1	-97.4
1 MHz	-132.1	-130.1	-125.5	-120.2
Residual Response Spurious Rejection on dBm	Frequency Range	R.L.=0 dBm	R.L.=-20 dBm	R.L.=-50 dBm
RBW =1 kHz	100 kHz~100 MHz	-90	-100	-125
	100 MHz~6.3 GHz	-90	-98	-110
Residual Response Spurious Rejection off dBm; RBW =1 kHz	100 kHz~100 MHz	-75	-95	-115
	100 MHz~6.3 GHz	-85	-95	-108
Image Frequency Suppression	>80 dBc (spurious rejection on), >35 dBc (spurious rejection off, typical value)			
Local Oscillator Related Spurious	<-60 dBc (Offset Center Frequency +/- (N/M)*125 MHz, N/M = 1,2,3,4,5...)			
Signal Processing				
Analysis Bandwidth	Maximum 25 MHz, Decimate Factor:1			
IQ Data	31.25 MSPS Decimate factor: 1,2,4,8,16,32,64,128,256 supported (FPGA)			
Storage Depth	The built-in memory depth is 128 Mbytes Supports continuous and uninterrupted storage when the data generation rate is less than the bus bandwidth, and the storage depth is only limited by the hard disk capacity			
External trigger response	Maximum frequency response 500 times/sec			
Analog IF output	Not supported			
Amplitude				
Maximum safe input power (CW)	26 dBm	30 MHz~6.3 GHz the preamplifier off (R.L. ≥ 0 dBm)		
	10 dBm	100 kHz~30 MHz or preamplifier on (R.L. <0 dBm)		
Maximum DC Voltage	+/-15 VDC			
Display Range	DANL~26 dBm			
Amplitude Accuracy	+/- 1.5 dB			
IF in-band spectrum ripple	+/- 1.75 dB (25 MHz analog IF bandwidth)			
Reference level (R.L.)	-50 dBm~23 dBm			
RF Preamplifiers	set as automatically turn on or forcibly turn off			
VSWR	<1.7:1	30 MHz~6.3 GHz (R.L. ≥ 10 dBm)		
	<2.0:1	30 MHz~6.3 GHz (R.L. ≥ 0 dBm)		
	<2.5:1	30 MHz~6.3 GHz (R.L. ≥ -40 dBm)		

Display Average Noise Level (DANL) dBm/Hz RBW=10 kHz RMS detector	Frequency Range		R.L.= 0 dBm (IFGainGrade = 3)	R.L.=-20 dBm (IFGainGrade = 3)	R.L.=-50 dBm (IFGainGrade = 3)
	9 kHz		-120	-130	-145
	100 kHz		-130	-137	-147
	100 MHz~3.0 GHz		-127	-142	-158
3.0 GHz~6.3 GHz		-126	-139	-156	
Standard Spectrum Analysis					
Detector	Positive peak, Negative peak, Sampling, Average, RMS, Max Power				
RBW	0.1 Hz~2.5 MHz				
VBW	0.1 Hz~10 MHz				
Trace Function	Sample, Positive Peak, Negative Peak, Local average, Maximum hold, Minimum hold, Average				
Data Chart	SASudio4 software provides regular spectrum, waterfall chart, and historical trace				
Sweep speed - Standard Spectrum Analysis 100MHz-6.3GHz	>100 GHz/s	FPGA	RBW≥250 kHz, B-Nuttall window, spurious rejection: Standard		
Detection Analysis/Zero Span					
Highest Time Resolution	32 ns				
Maximum Analysis Bandwidth	25 MHz				
Detection	Positive peak, Negative peak, Sampling, Average, RMS, Max Power				
Real Time Spectrum Analysis					
FFT Analysis	Variable point FFT engine implemented by FPGA. Frame rate compression and trace detection are supported. There is strictly no gap and overlap between FFT frames.				
	FFT refresh rate= $10^9$ ns/(N * D * 32 ns); POI = 2*N*D*32 ns N is the number of FFT points (2048,1024,512,256,128,64,32), and D is the decimate factor (1, 2, 4, 8...)				
	Typical Settings		FFT Refresh Rate	POI	
	N =2048, D = 1		15,258 times/sec	131.072 us	
N = 32, D = 1		976,563 times/sec	2.048 us		
Real-time Analysis Bandwidth	25 MHz				
Window Function	B-Nuttall, FlatTop				
RBW	3.68 MHz-3.59 kHz (FlatTop window); 1.95 MHz~1.90 kHz (B-Nuttall), 11 grades for each window type				
Amplitude Resolution	0.75 dB				
Signal generator (option)					
Frequency range	100 kHz~6.3 GHz, 10 Hz for each step				
Power range	-50 dBm~0 dBm, 0.25 dB for each step				
VSWR	<2.0:1		30 MHz~6.3 GHz		
Non-harmonic spurs	<-50 dBc				
Harmonic wave	100kHz~30MHz	30MHz~1.6GHz	1.6GHz~3GHz	3GHz~3.2GHz	3GHz~6.3GHz
Second harmonic	<-10 dBc	<-10 dBc	<-20 dBc	<-20 dBc	<-20 dBc
Third harmonic and above	<-10 dBc	<-10 dBc	<-20 dBc	<-20 dBc	<-20 dBc
Signal leakage to receiver	100 kHz~30 MHz		>90 dBc		
	30 MHz~3 GHz		>80 dBc		
	3 GHz~6.3 GHz		>70 dBc		

General		
Input And Output	Power Supply	Type-C (1), dedicated power supply port, please provide 5 V 2 A peak power supply capacity Allowable voltage range: 4.75~5.25 V, ripple less than 200 mVpp
	Data	Type-C (2), USB3.0 (USB2.0 available but bandwidth limited)
	RF input	SMA (F), Input impedance 50 Ω
	External reference clock input	MCX (F), amplitude ≥ 1.5 Vpp, input impedance 330 Ω
	External reference clock output	Not supported
	External trigger input	Integrated in MUXIO (Type-C), 3.3 V CMOS, input: high impedance
	External trigger output	Integrated in MUXIO, 3.3 V CMOS
	Analog IF output	Not supported
Power Consumption	Peak: 10 W, typical: 7 W~10 W, power port (5V 2A Max), data port (5V 1A Max)	
Operating Temperature (ambient temperature /device core temperature)	0~50 °C/0~70 °C (Standard temperature class)	
	-20~65 °C/-20~85 °C (Extended Temperature Class Option) (plastic enclosure and fan not included)	
	-40~65 °C/-40~85 °C (Wide Temperature Class Option) (plastic enclosure and fan not included)	
Storage Temperature (ambient temperature)	-20~70 °C (Standard temperature class)	
	-40~85 °C (Extended temperature class and wide temperature options) (plastic enclosure and fan not included)	
Weight and size	Size: 142x54x16 mm, weight:168 g (Excluding protective case and structural fittings, including connector length) Size: 156x62x22 mm, weight:296 g (Including protective case and structural fittings, including connector length)	
Accessories	Flash disk×1, USB cable×2, Power adaptor×1	

\*The typical values of the indicators are applicable for the following conditions: (1) Start up and warm up for 20 minutes; (2) Ambient temperature 25 °C (device core temperature 50 °C); (3) Standard sweep mode-Spurious rejection on; (4) 25 MHz analysis bandwidth and IFGainGrade=3; (5) The user shall provide the necessary heat dissipation conditions to ensure that the ambient temperature and the core temperature of the equipment are within the rated range at the same time.

Code	Option	Explanation
01	Built-in OCXO reference clock (hardware opt.)	Providing a reference clock with better stability than the standard configuration, with a temperature drift of <0.15 ppm, increasing the overall power consumption by 0.8 W.
02	Built-in analog signal generator	100 kHz-6.3 GHz signal generator
10	IO extension board (accessory)	Converting the MUXIO interface into multiple MMCX and board to wire connector to facilitate the connection of trigger input, output, and other signals.
11	External GNSS (accessory)	Standard GNSS module connected to MUXIO.
12	External high precision GNSS (accessory)	High precision GNSS module connected to MUXIO.
13	External GNSS disciplined OCXO reference clock (accessory)	Providing GNSS disciplined reference clock and 1PPS, increasing the overall power consumption by 1.1W.
20	Extended temperature class (hardware opt.)	- 20~65 °C/- 20~85 °C(Extended temperature class opt.)
21	Wide temperature class (hardware opt.)	- 40~65 °C/- 40~85 °C(Wide temperature class opt.)

Distribution in the UK & Ireland



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